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<b>Notice of Allowability</b>	Application No.	Applicant(s)	
	09/784,274	PETING ET AL.	
	Examiner	Art Unit	
	Zhuo H Li	2186	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to August 12, 2004.
2. ☒ The allowed claim(s) is/are 1-31.
3. ☐ The drawings filed on \_\_\_\_\_ are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  6. ☒ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☒ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☒ to Paper No./Mail Date June 2, 2003.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

- |   |   |
|---|---|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)   | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)           |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____ |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment                              |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material          | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance  |
|   | 9. <input type="checkbox"/> Other _____   |

## EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

1. Claims 1-31 are allowed.
2. The following is an examiner's statement of reasons for allowance:

Applicant's invention is drawn to a method and apparatus for writing burst data to memory in order to save time by performing the burst write to memory in few system clock cycles in a system that employs a shared bus architecture (i.e., storing a first portion of the write data burst in a buffer, concurrently with activating the at least one memory unit to receive data, if the at least one memory unit is not available to receive data, writing a second portion of the write data burst to the at least one memory unit when the at least one memory unit is available to receive data without storing the second portion in the buffer and writing the first portion of the write data burst from the buffer to the at least one memory unit after writing the second portion of the write the burst).

Applicant's independent claims 9, 17, 20, 24 and 28 each recites, *inter alia*, a computer system (200) with a structure defined in the specification (pages 7-11) comprising a buffer (330) communicatively coupled to the circuit to temporarily store a first portion of the write data burst if the at least one memory unit (215) is not available to receive data, and a circuit to concurrently detect availability of the memory unit, to activate the at least one memory unit to store a second portion of the write data burst without storing the second portion in the buffer, the circuit to further store the first portion of the write data burst from the buffer to the at least one memory unit after storing the second portion of the write data burst in the at least one memory unit when

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the at least one memory is available to receive data. Applicant's invention of claims 9, 17, 20, 24 and 28 comprise a particular combination of elements, which is neither taught nor suggested by the prior art.

Applicant's independent method claim 1 recites, *inter alia*, the steps of storing a first portion of the write data burst in a buffer, concurrently with activating the at least one memory unit to receive data, if the at least one memory is not available to receive data, writing a second portion of the write data burst to the at least one memory unit when the at least one memory unit is available to receive data without storing the second portion in the buffer, and writing the first portion of the write data burst from the buffer to the at least one memory unit after writing a second portion of the write data burst. These steps, in combination with the remaining steps of claims 6 and 7, are neither taught nor suggested by the prior art.

Accordingly, applicant's claims are allowed for these reasons and for the reasons recited by Applicant's amendment (8/12/2004) and response (12/2/2003).

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Zhuo H Li whose telephone number is 703-305-3846. The examiner can normally be reached on Tue-Fri 8:00 a.m. to 5:30 p.m..


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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Kim can be reached on 703-305-3821. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

4. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Zhuo H. Li

September 2, 2004

  
MATTHEW KIM  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2100